

38. (New) The system of Claim 37, wherein the link layer and the physical layer of the transmitter each comprise portions of a programmable logic device (PLD).

39. (New) The system of Claim 38, wherein the PLD is a field programmable gate array (FPGA).

40. (New) The system of Claim 37, further comprising:

a receiver coupled to the network and adapted to receive data from the network, the receiver being implemented in hard logic, the receiver comprising:

A1 a physical layer having a data node adapted to receive a packet assembly from the network, the packet assembly including at least a portion of the data; and

a CRC generator adapted to perform a CRC on at least a portion of the packet assembly to provide a calculated CRC value.

41. (New) The system of Claim 40, wherein the link layer and the physical layer of the transmitter, and the physical layer and CRC generator of the receiver, each comprise portions of a programmable logic device (PLD).

42. (New) The system of Claim 41, wherein the PLD is a field programmable gate array (FPGA).

43. (New) A system, comprising:

a network; and

a receiver coupled to the network and adapted to receive data from the network, the receiver being implemented in hard logic, the receiver comprising:

a physical layer having a data node adapted to receive a packet assembly from the network, the packet assembly including at least a portion of the data; and

a CRC generator adapted to perform a CRC on at least a portion of the packet assembly to provide a calculated CRC value.

A1 44. (New) The system of Claim 43, wherein the physical layer and CRC generator of the receiver each comprise portions of a programmable logic device (PLD).

45. (New) The system of Claim 44, wherein the PLD is a field programmable gate array (FPGA).

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